

---

# Transistors

**ECE 320 Electronics I**

**Jake Glower - Lecture #11**

Please visit [Bison Academy](#) for corresponding lecture notes, homework sets, and solutions

---

---

## **NPN Transistor Theory**

Transistors are similar to diodes in that they are made up on n-type and p-type silicon. They differ in that

- Transistors are 3-terminal devices (NPN or PNP),
  - Transistors can operate in three states: off, active, and saturated,
  - Transistors can be used as a switch to turn a device on and off electrically, and
  - Transistors can be used as an amplifier (a current controlled current amplifier).
-

---

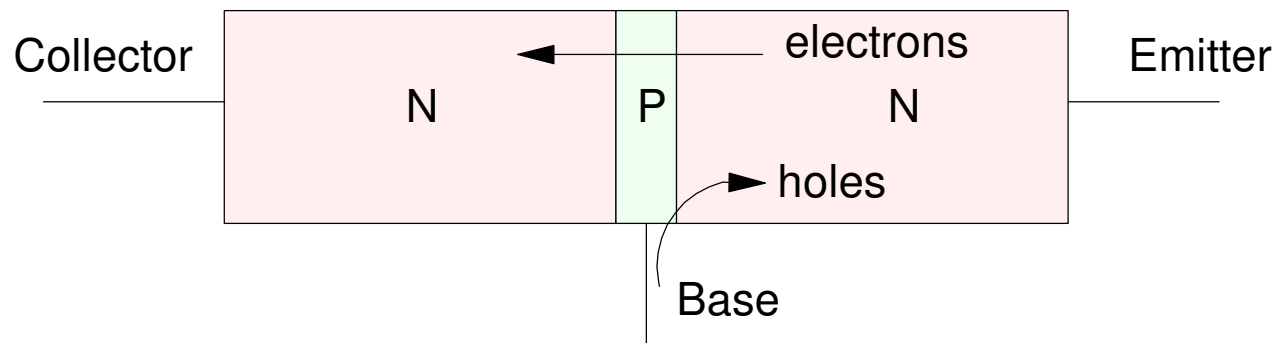
## NPN Transistor Operation

- A reverse biased diode (np) blocks current from emitter to collector:
- Base to emitter is a diode (pn junction)

If you apply current base to emitter

- Holes flow from the base to the emitter
- Electrons flow from the emitter to the base (to the collector)

If the emitter has 100x the doping of the base, you get current gain

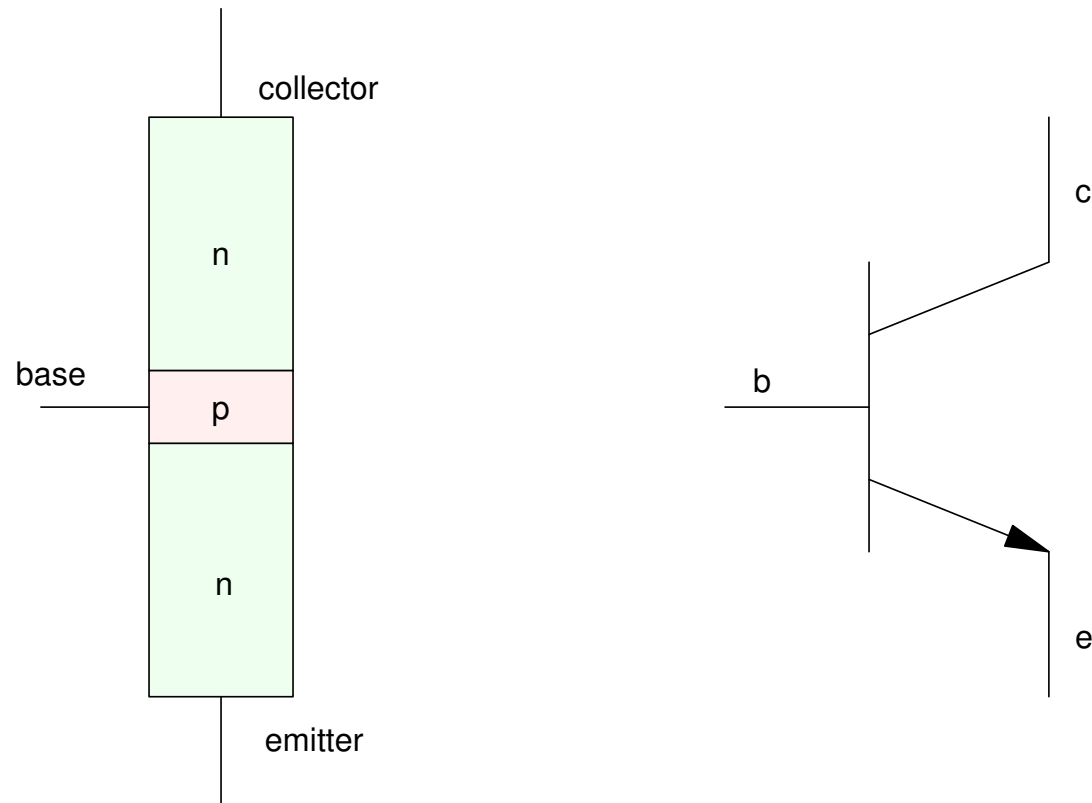


An NPN device makes a transistor

---

---

The symbol for a transistor is as follows. Note that the arrow represents the diode from base to emitter. It reminds you which way current flows.



Symbol for an NPN transistor: the arrow indicated the diode from base to emitter

---

# Transistor VI Characteristics:

Off:

$$I_b = I_c = 0$$

Active:

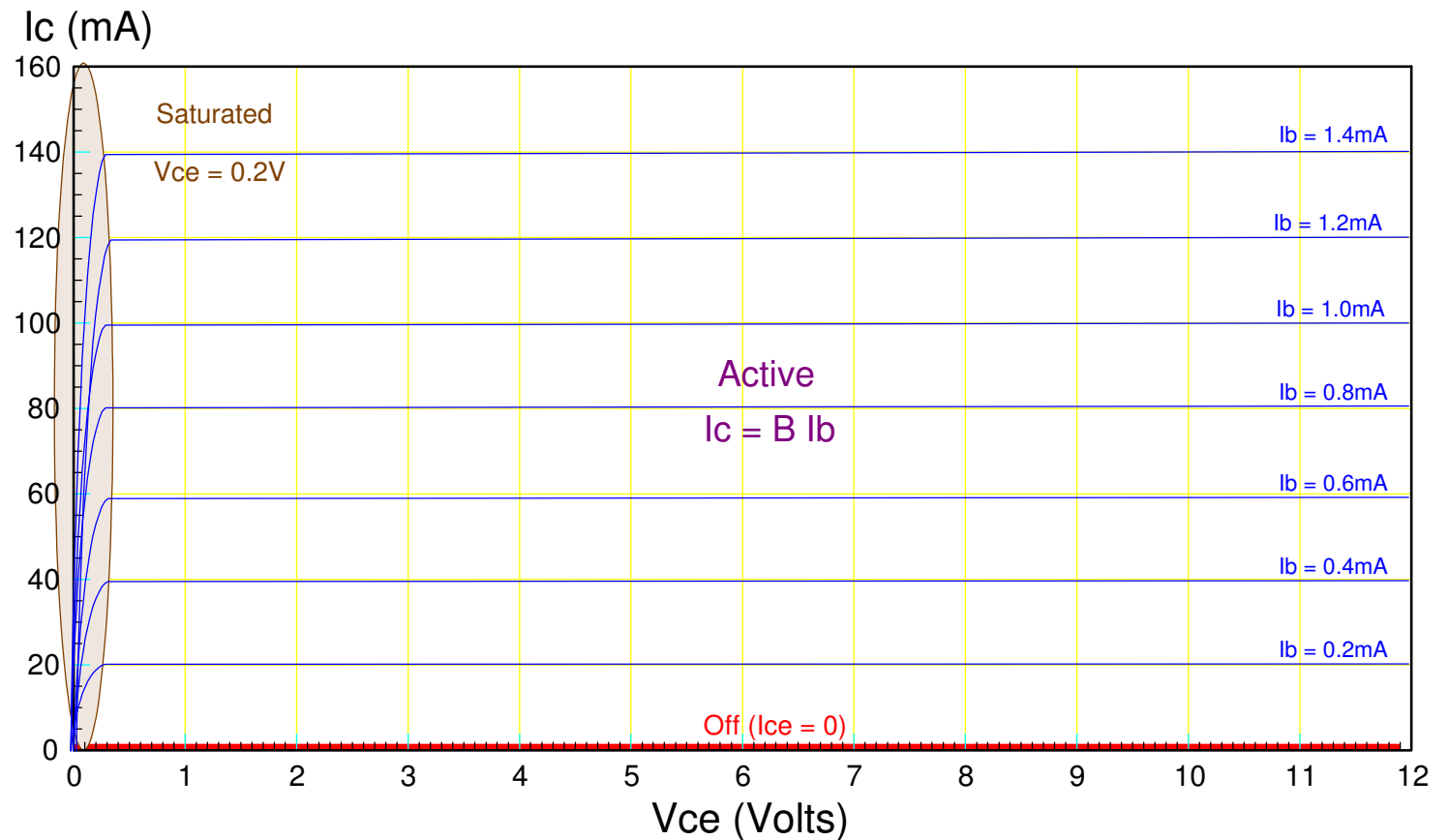
$$I_c = \beta I_b$$

$$\beta = \frac{140\text{mA}}{1.4\text{mA}} = 100$$

Saturated:

$$\beta I_b > I_c$$

$$V_{ce} \approx 0.2\text{V}$$



## Off State

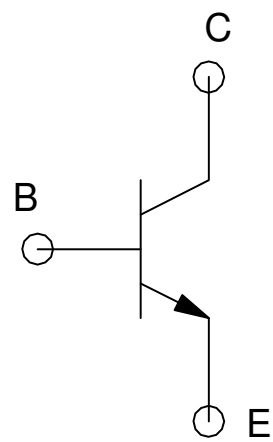
- $I_b = 0$ ,  $I_c = 0$

Active State:  $0.2V < V_{ce} < V_{cc}$

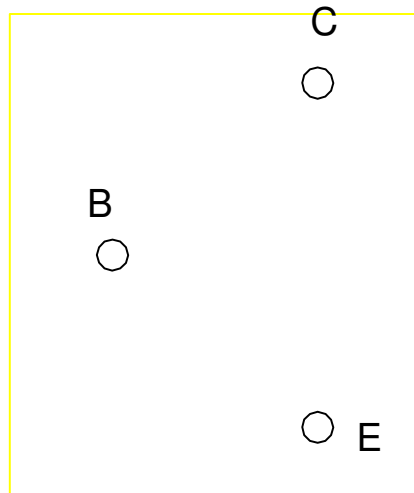
$$I_c = \beta I_b.$$

Saturated State:  $\beta I_b > I_c$

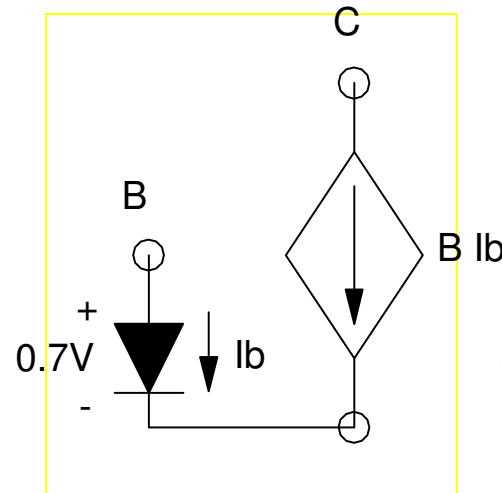
$$V_{ce} = 0.2V$$



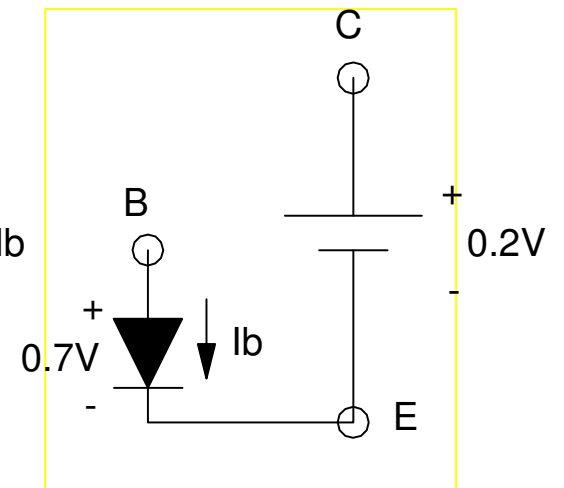
NPN Transistor



Off State



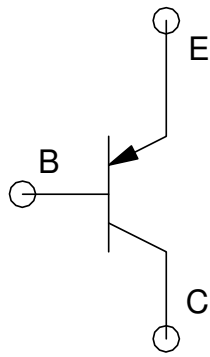
Active State



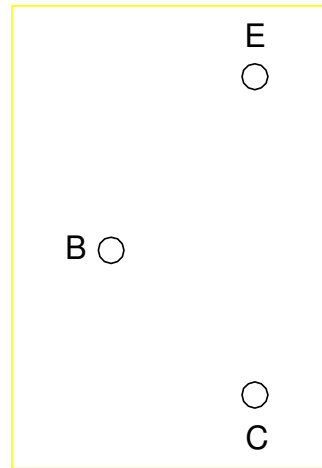
Saturated State

# PNP Transistors:

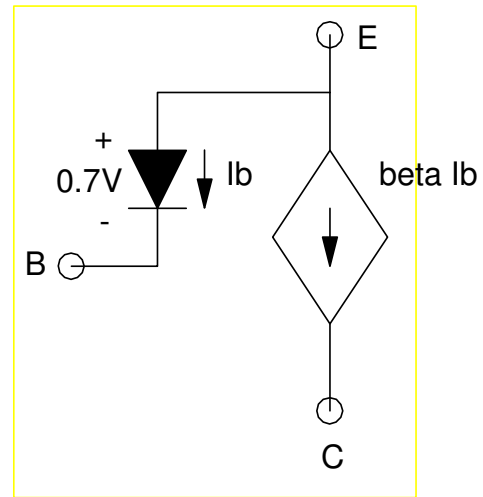
- Note: The arrow represents a diode
- This current determines  $I_{ce}$  (or  $I_{ec}$ )



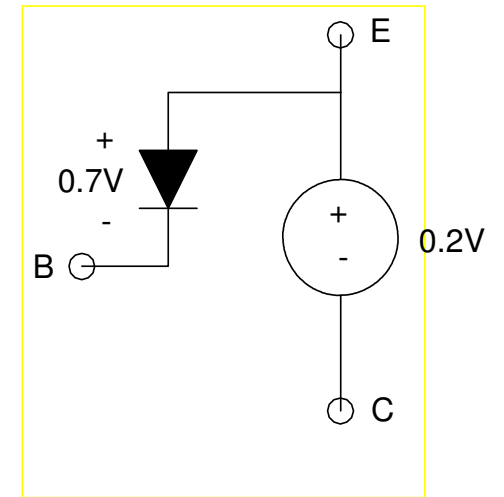
PNP Transistor



Off State

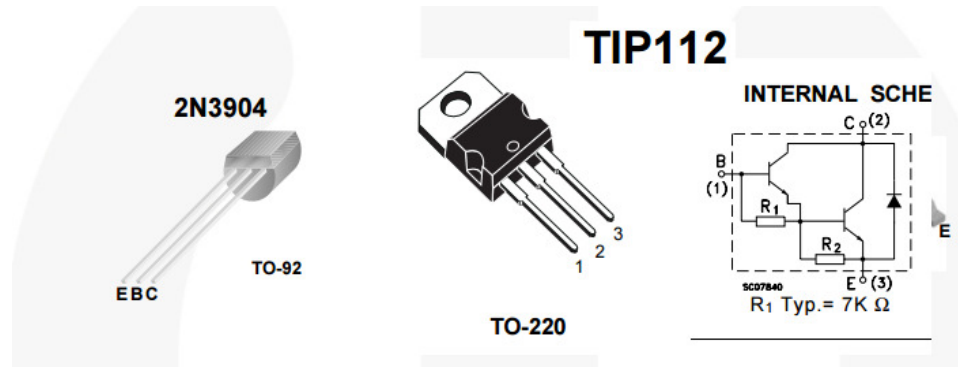


Active State



Saturated State

# Transistor Specs



	3904 NPN	6144 NPN	TIP112 NPN
hfe (beta)	100 - 300	200	1,000
I <sub>c</sub> (max)	200mA	10A	4A
V <sub>be</sub>	700mV	700mV	1.4V
V <sub>ce</sub> (max)	40V	50V	40V
V <sub>ce</sub> (sat)	200mV	360mV	900mV
cost (ea)	\$0.03	\$0.37	\$0.59



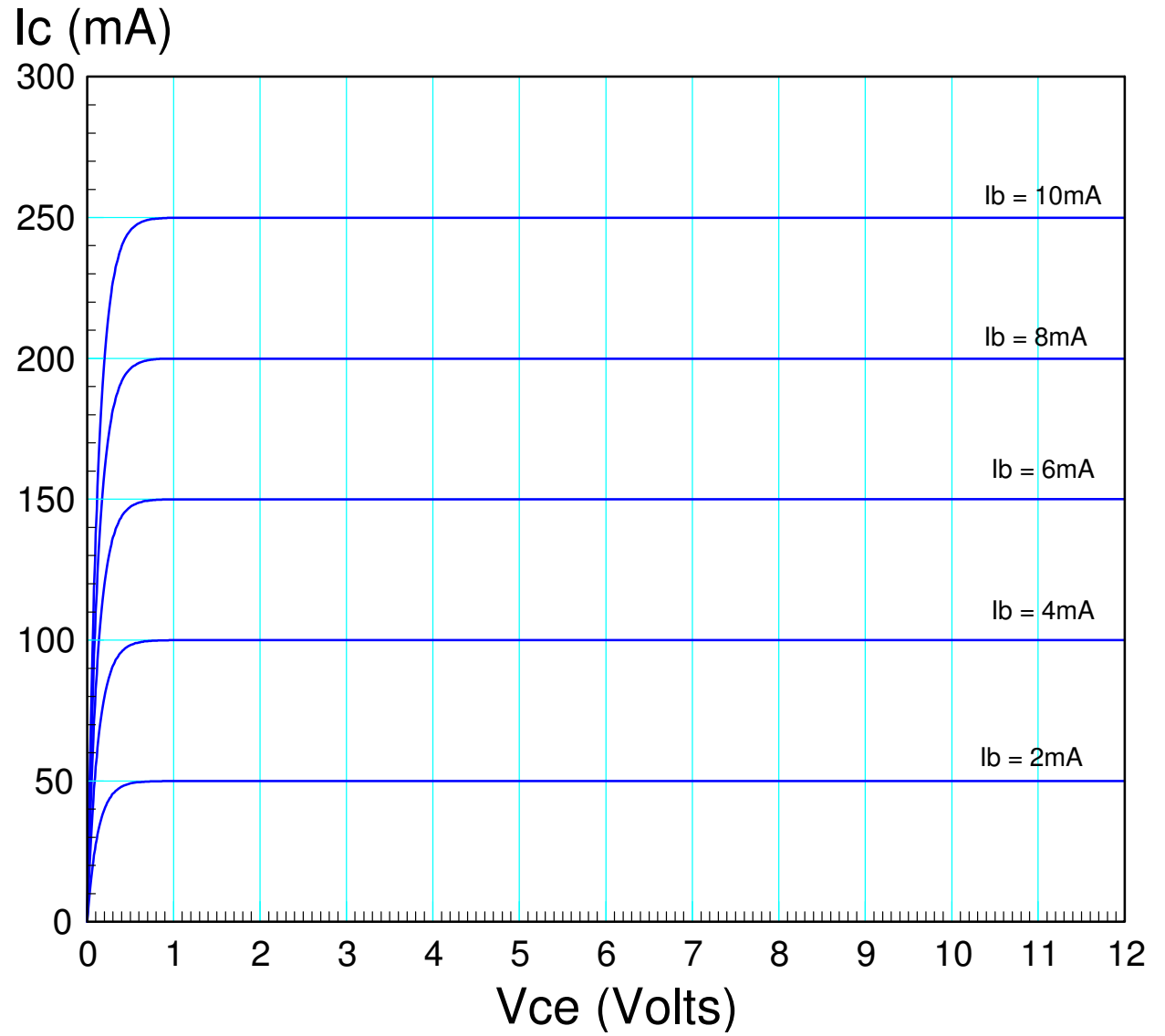
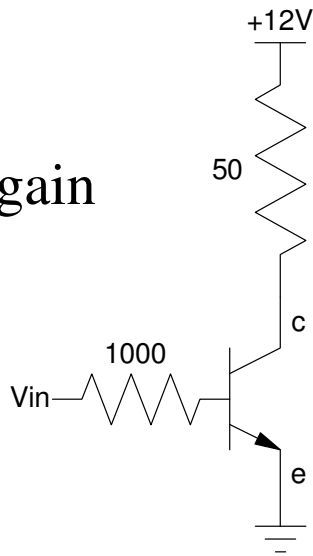
# Handout

Label the regions

- Saturated
- Active
- Off

Determine the gain

- $h_{fe}$  or  $\beta$

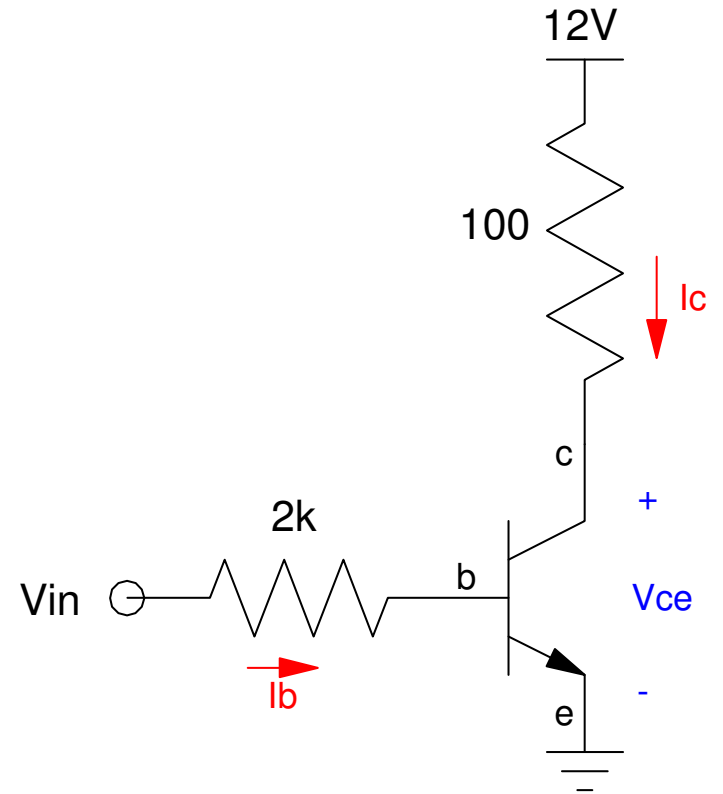
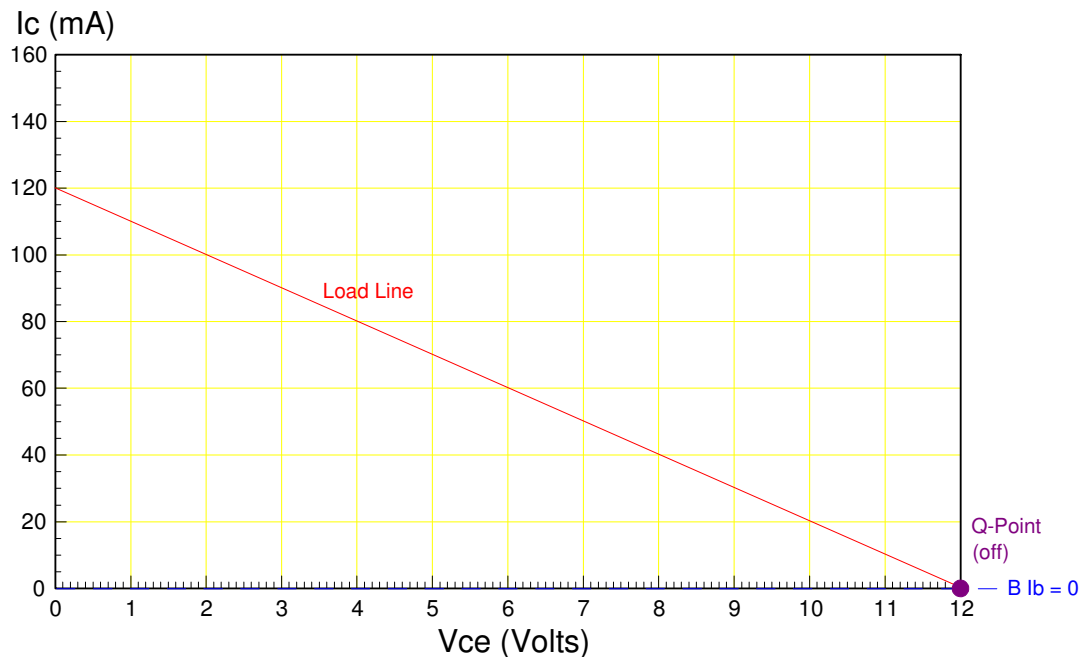


# Transistor Circuit Analysis:

a)  $V_{in} = 0.5V$  (Off State):

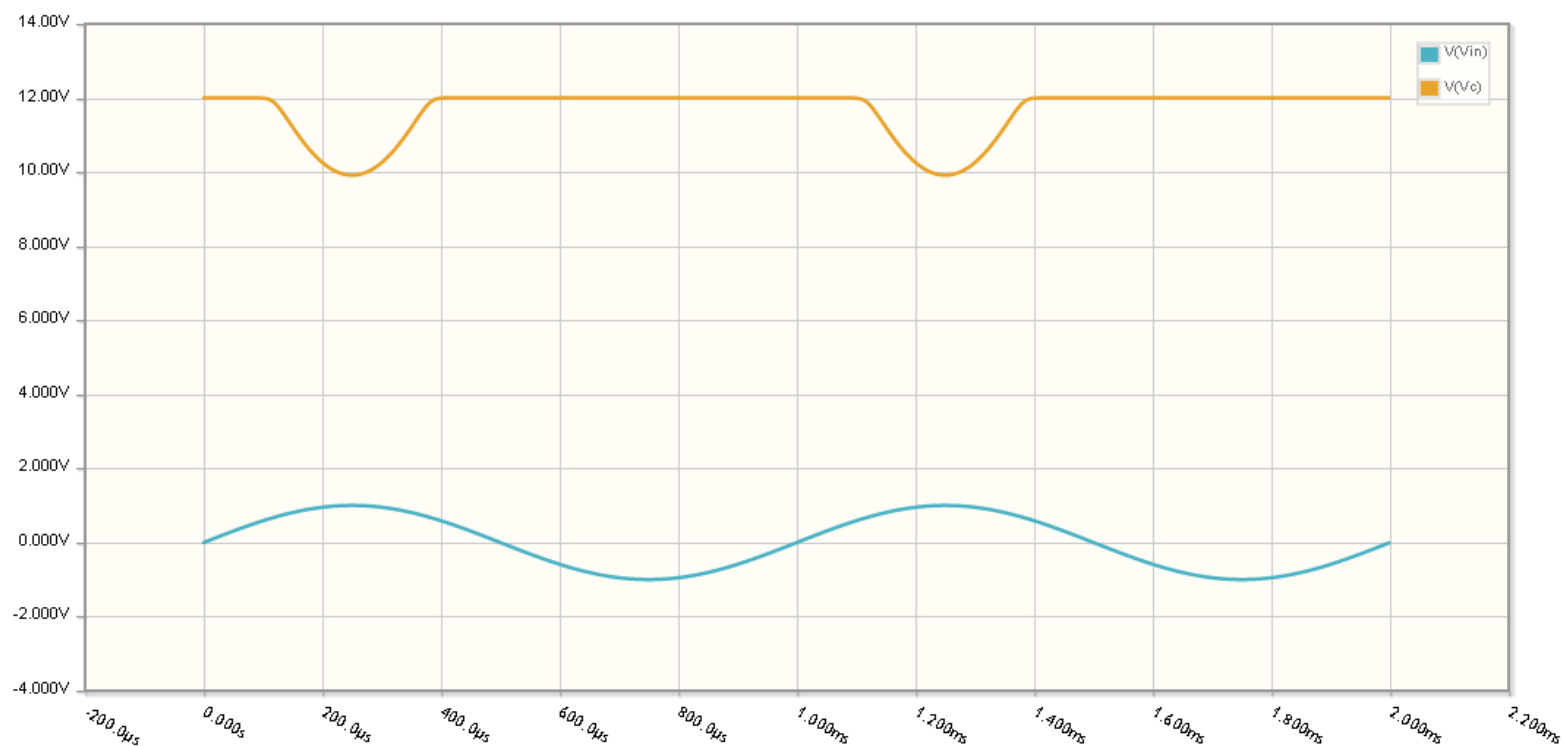
- $I_b = 0$
- $I_c = 0$
- $V_{ce} = 12V$

The transistor is off.



## Off State in CircuitLab

- If  $I_b$  (or  $I_c$ ) tries to go negative, it clips at 0
- $V_c$  clips at +12V



$V_{in}$  (blue) and  $V_c$  (orange) for a 1Vp sine wave centered at 0V.  
When  $V_c = 12V$  the transistor turns off.

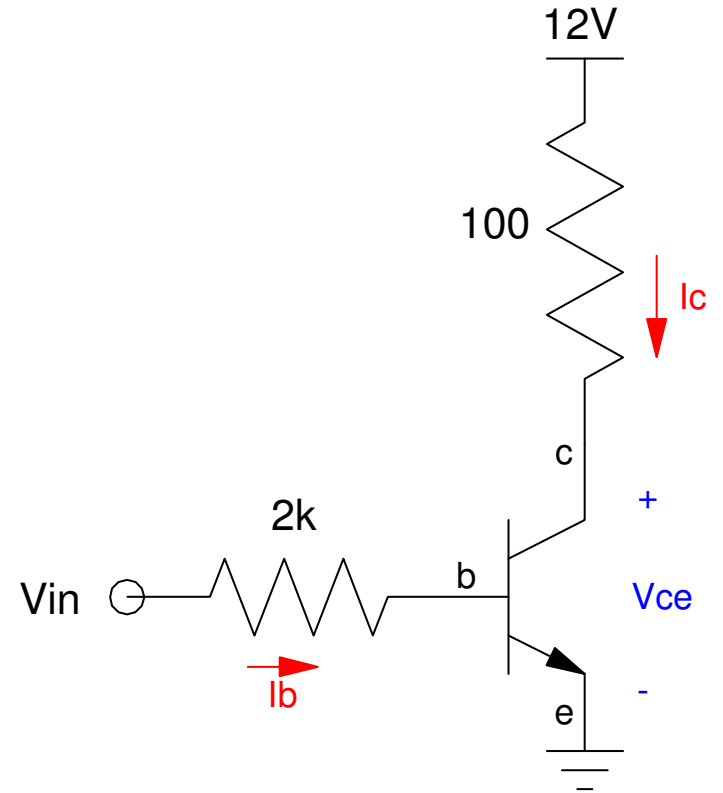
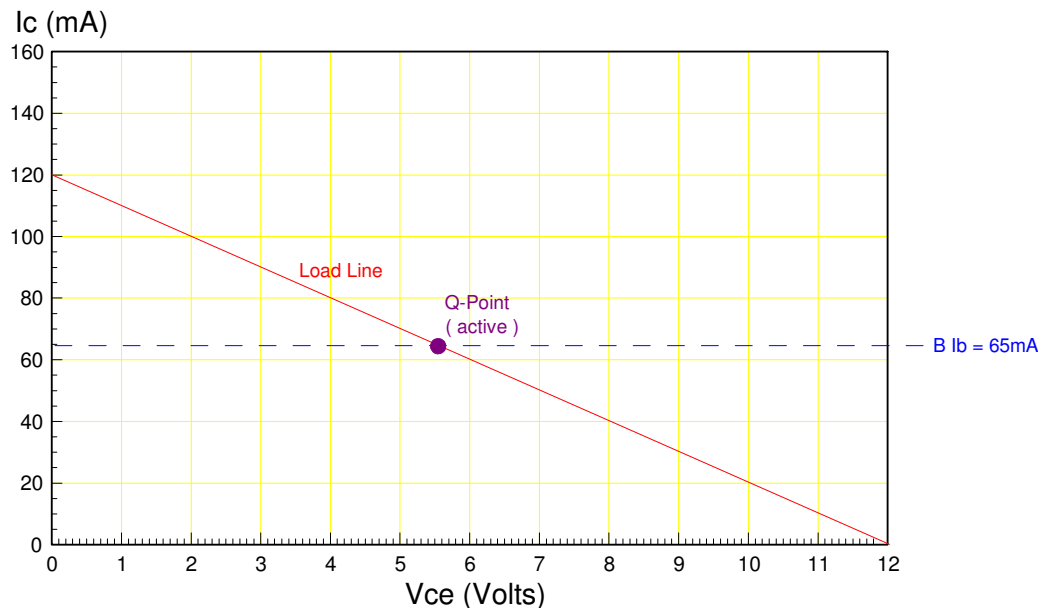
**b)  $V_{in} = 2.0V$  (Active Region).**

$$V_b = 0.7V \quad (\text{diode is on})$$

$$I_b = \left( \frac{2V - 0.7V}{2k\Omega} \right) = 650\mu A$$

$$I_c = \beta I_b = 65mA$$

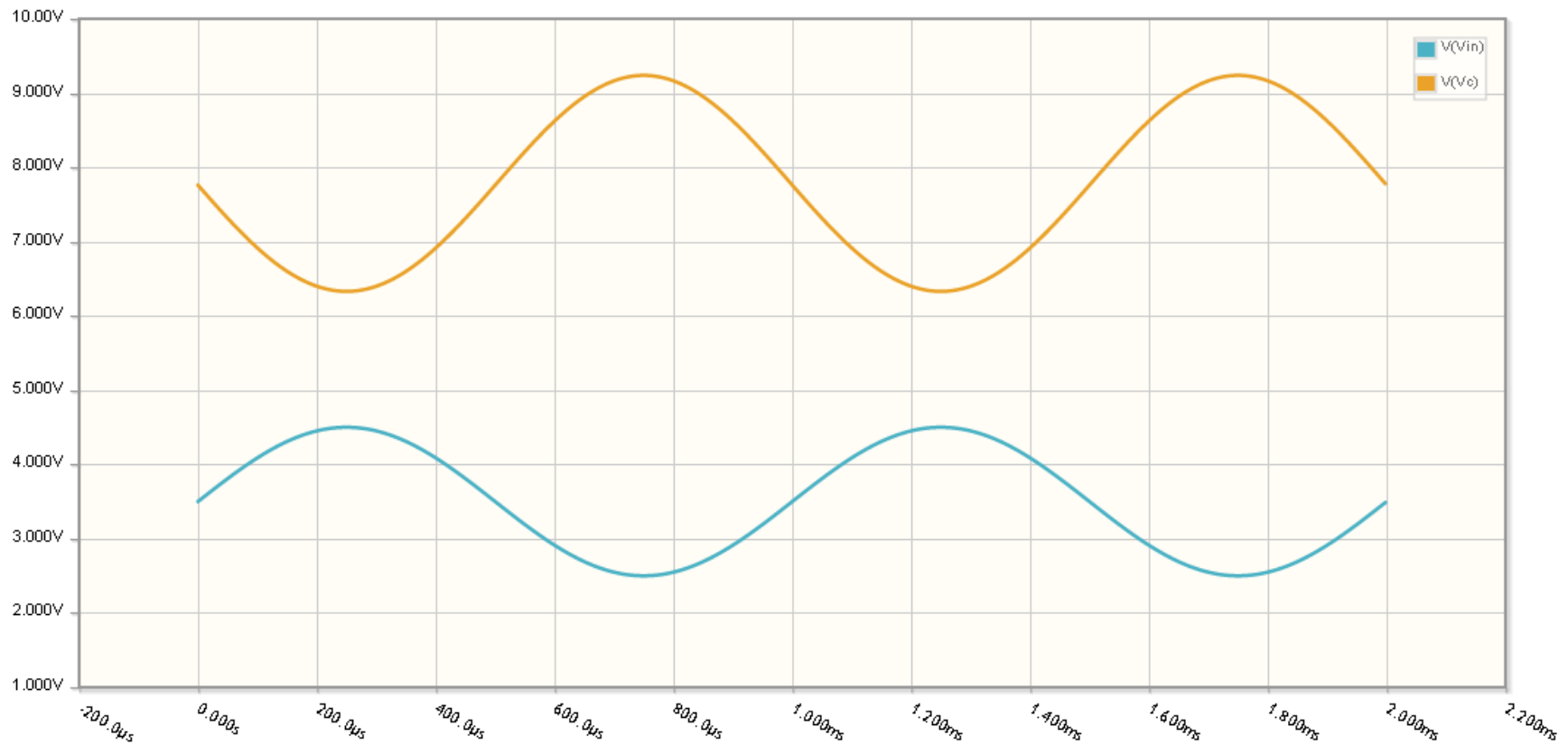
$$V_{ce} = 12 - 100I_c = 5.5V$$



---

## Active Region:

- Increase the DC offset of  $V_{in}$  (blue)
- Increases  $I_c$  (decreases  $V_c$ )



**c)  $V_{in} = 3.5V$  (Saturated Region).**

$$I_b = \left( \frac{3.5V - 0.7V}{2k\Omega} \right) = 1.4mA$$

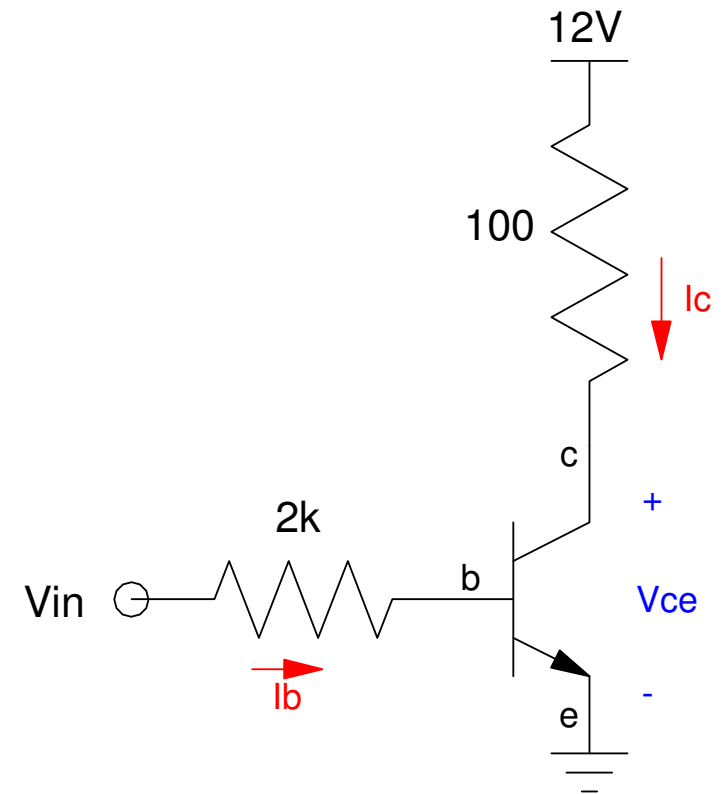
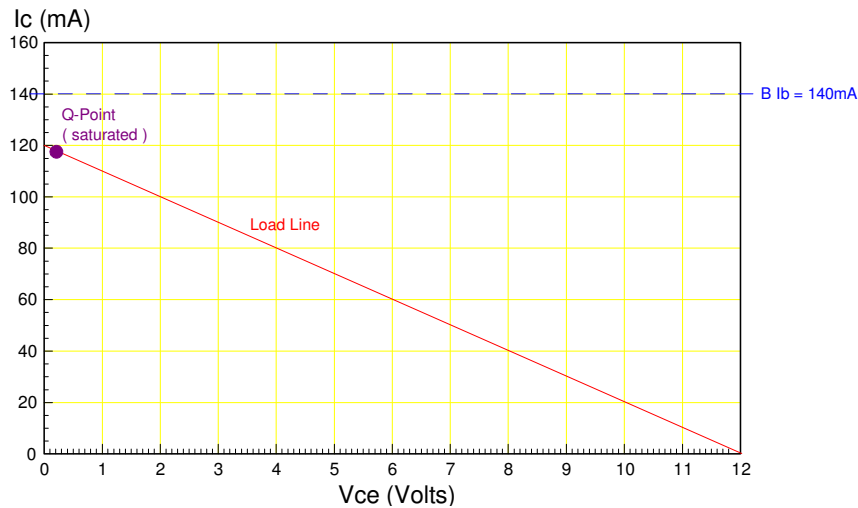
$$I_c = \beta I_b = 140mA$$

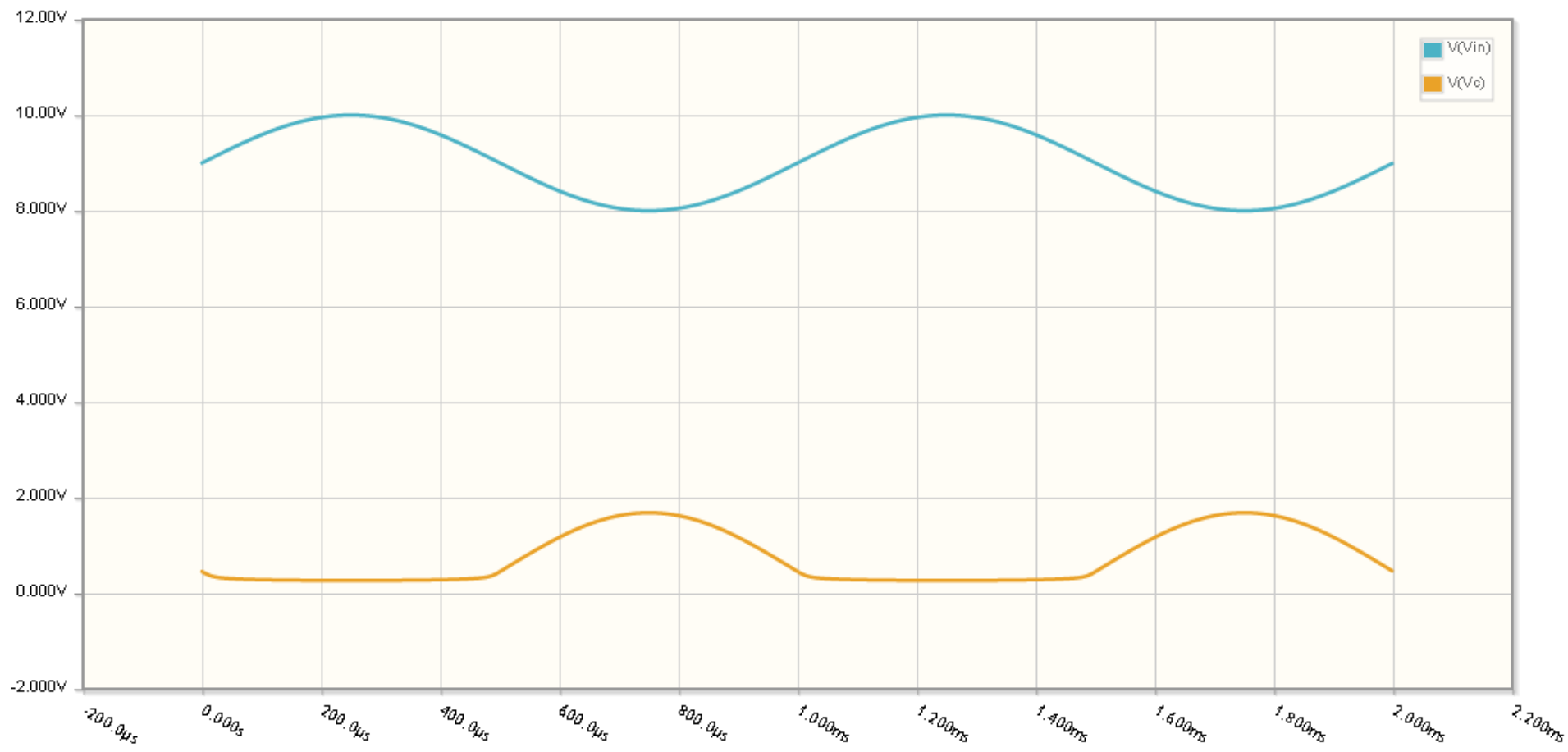
$$V_{ce} = 12 - 100I_c = -2.0V$$

Not Possible

$$V_{ce} = 0.2V$$

$$I_c = \left( \frac{12V - 0.2V}{100} \right) = 118mA$$





---

# Permutations

N diodes in a circuit

- $2^N$  permutations of on / off

N transistors in a circuit

- $3^N$  permutations of off / active / saturated

It helps to know the answer to find the answer

- ECE 320 Digital Electronics: Usually transistors are on or off
  - ECE 321 Analog Electronics: Usually transistors are active
-

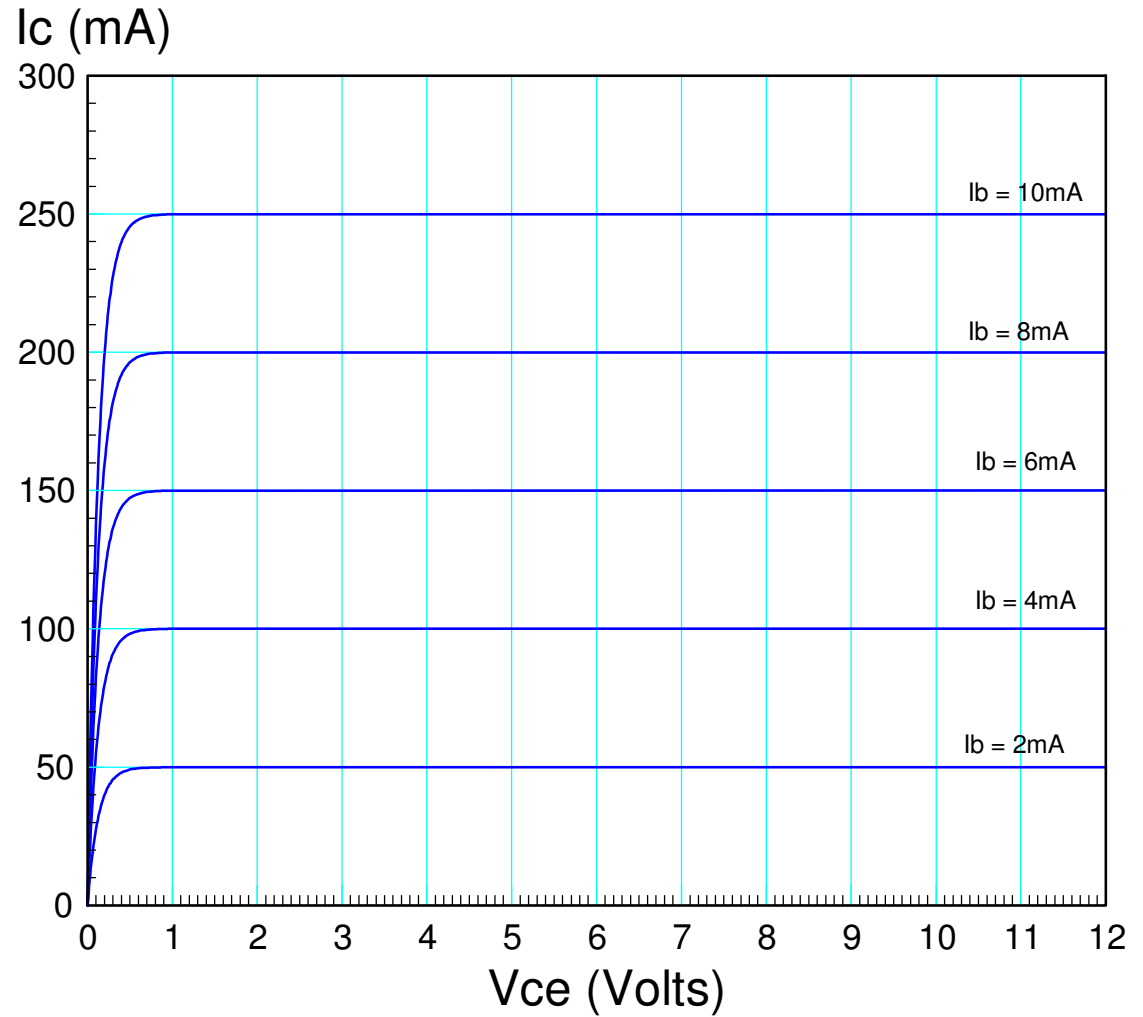
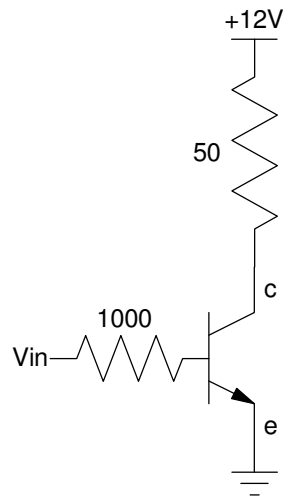


# Handout

Draw the Load Line

Determine ( $V_{ce}$ ,  $I_c$ ) when

- $V_{in} = 0V$
- $V_{in} = 5V$
- $V_{in} = 10V$
- $V_{in} = 15V$



---

## Summary

An npn or pnp device is a transistor.

Transistors have 3 modes of operation

- off / active / saturated

Results in  $3^n$  circuits to analyze

- $n$  = number of transistors
- one of these circuits will be correct

On / Off is primarily used in digital electronics

Active is primarily used in analog electronics

---