
Boolean Logic

ECE 320 Electronics I

Jake Glower - Lecture #19

Please visit [Bison Academy](#) for corresponding lecture notes, homework sets, and solutions



Boolean Logic

- Black and White world ("for us or against us")
- Either true (logic 1) or false (logic 0)
- Other forms of logic exist (ex. fuzzy logic)



Sith Lords: <https://wallpaperplay.com/board/sith-lord-wallpapers>

AND: Y is true if A and B are both true. It's false otherwise.

OR: Y is true if either A or B is true.

NOT: Y = NOT A means that whatever A is, Y is the opposite.

DeMorgan's Theorem:

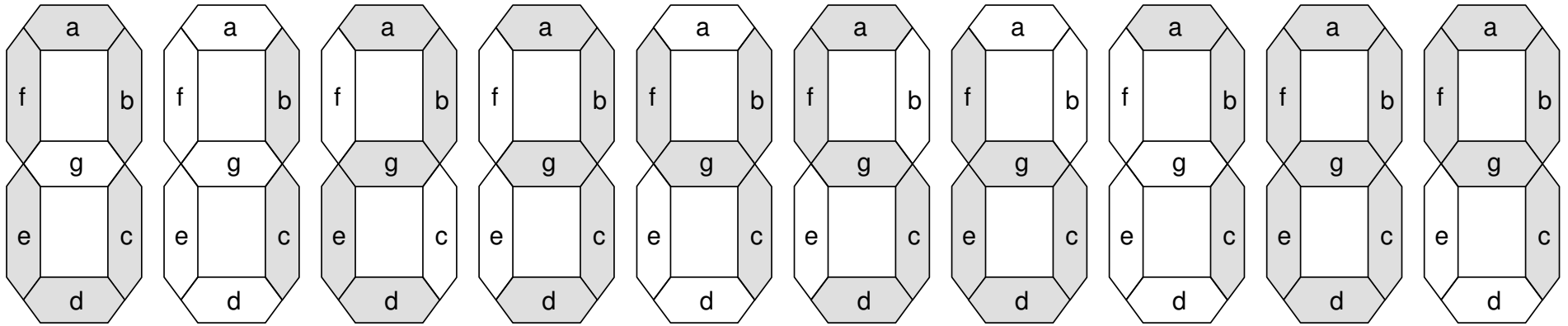
$$\overline{AB} = \overline{A} + \overline{B}$$

$$\overline{A + B} = \overline{A} \cdot \overline{B}$$

A	B	Y = AB AND	Y = (AB)' NAND	Y = A+B OR	Y = (A+B)' NOR	A' NOT
0	0	0	1	0	1	1
0	1	0	1	1	0	1
1	0	0	1	1	0	0
1	1	1	0	1	0	0

Implementing Logic Using NAND Gates

- Input = 4 digital signals (ABCD)
- Output: 0 = light off, 1 = light on
- Relationship: LED (a) for a 7-segment display



Karnough Maps:

		Ya(ABCD)			
		CD			
		00	01	11	10
AB	00	1	0	1	1
	01	0	1	1	0
	11	x	x	x	x
	10	1	1	x	x

Circle the ones to generate Ya:

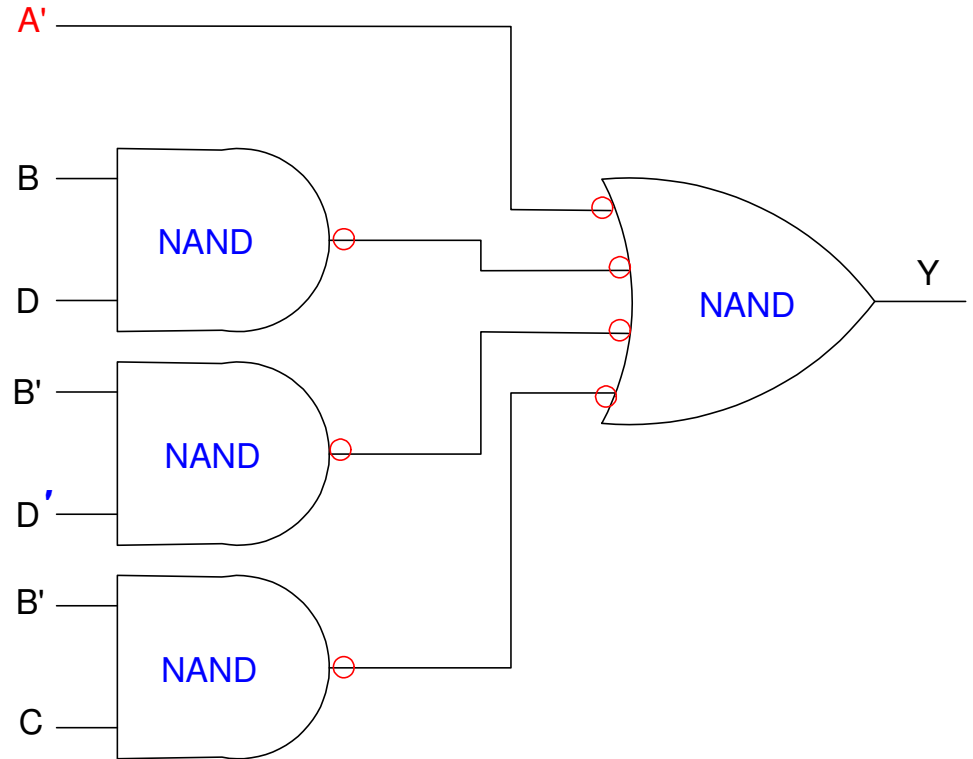
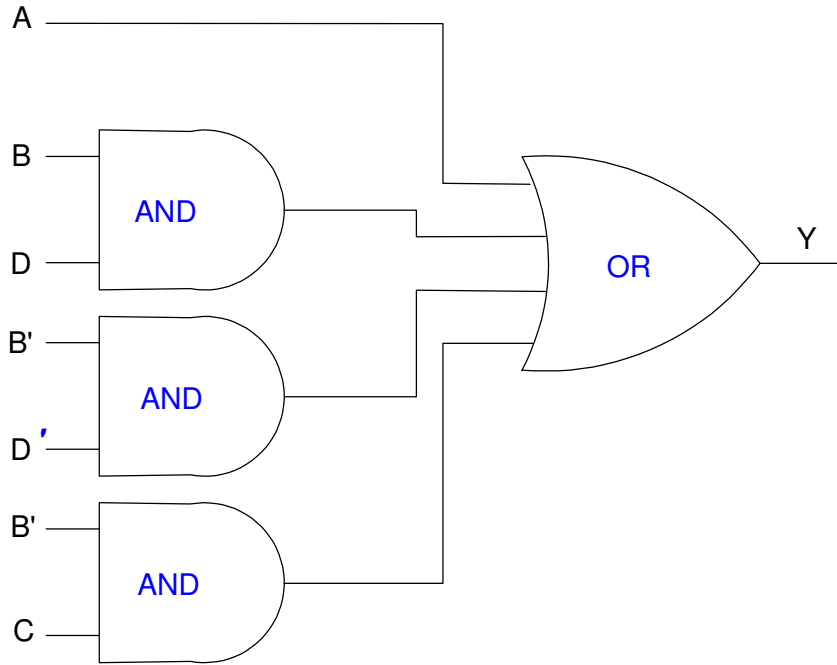
$$Y = A + BD + \overline{B}\overline{D} + \overline{B}C$$

		CD			
		00	01	11	10
AB	00	1	0	1	1
	01	0	1	1	0
	11	x	x	x	x
	10	1	1	x	x

Implement this using AND and OR gates

$$Y = A + BD + \overline{B}\overline{D} + \overline{B}C$$

To convert to NAND gates, add in a double-negative



Implementation using NOR Gates

Circle the zeros

$$\bar{Y} = \bar{A}\bar{B}\bar{C}D + B\bar{D}$$

Use DeMorgan's theorem

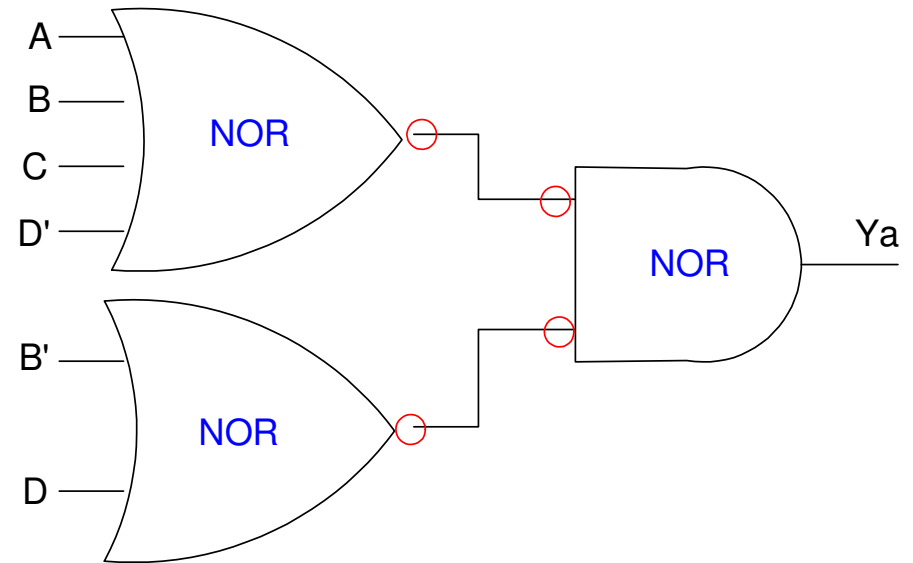
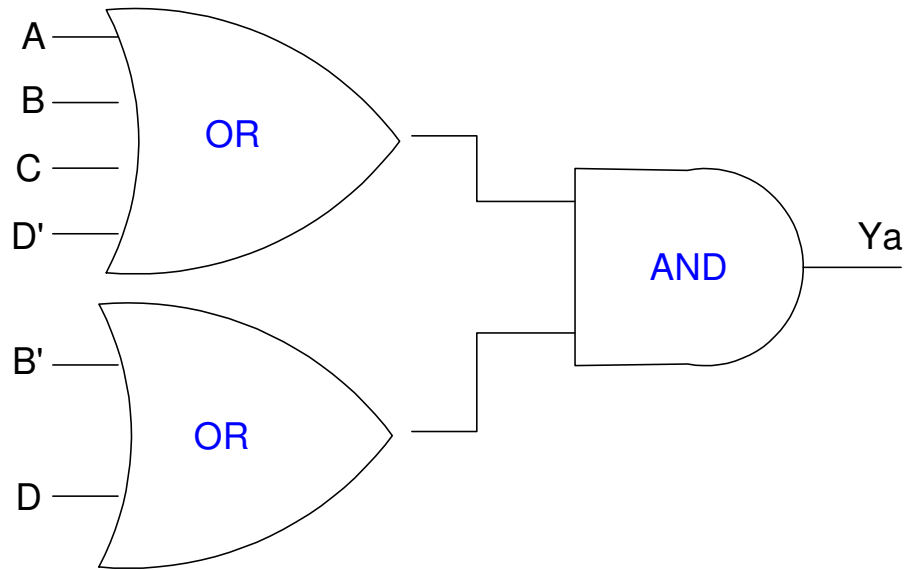
$$Y = \overline{\bar{A}\bar{B}\bar{C}D + B\bar{D}}$$

$$Y = (A + B + C + \bar{D})(\bar{B} + D)$$

		CD			
		00	01	11	10
AB	00	1	0	1	1
	01	0	1	1	0
	11	x	x	x	x
	10	1	1	x	x

Implement using OR and AND gates

- $Y = (A + B + C + \bar{D})(\bar{B} + D)$
- Add in a double-negative to turn these into NOR gates



Summary:

- You can implement any logic using NAND or NOR gates.
- The only difference is if you prefer circling the ones (NAND) or zeros (NOR).

Next lectures

- How to build NAND and NOR gates at the transistor level
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