TTL Logic

4) Determine the voltges and currents for the following TTL gate. Assume

-

DTL Logic

6) Determine the voltages and currents when V0 = 1V. Assume

- Ideal transistors (Vbe = 0.7V, Vce(sat) = 0.2V, gain = 100)
- Ideal silicon diodes (Vf = 0.7V)
- R = 800 + 100(Birth Month) + (Birth Day).

R 800 + 100*mo +day	V1	V2	V3	I4	I5
1314	**5.00V**	**1.40V**	**0.20V** saturated: 0.2V active: 5 - 20*I5	**2.74mA**	**240mA** min(I4*100, 240mA) active / saturated