

ECE 320 - Homework #9

MOSFET Switches, CMOS logic. Due Monday, March 21st

MOSFETs

1) The VI characteristics for an n-channel MOSFET are shown below.

- Label the off / ohmic / and saturated regions
- Determine the transconductance gain, k_n . Assume $V_{th} = 1.00V$

Pick a point in the saturated region

$$V_{gs} = 7V, I_{ds} = 290mA, V_{ds} = 10V$$

Solve for k_n

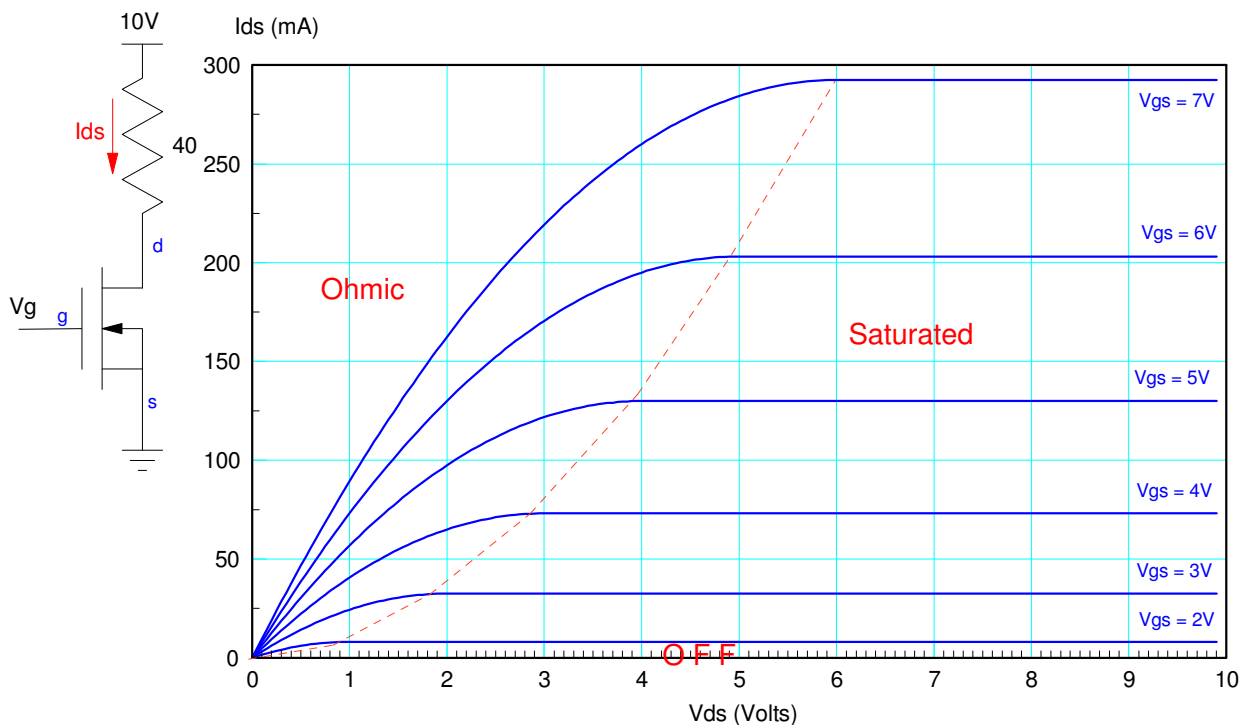
$$I_{ds} = \left(\frac{k_n}{2}\right)(V_{gs} - V_{th})^2$$

$$290mA = \left(\frac{k_n}{2}\right)(7V - 1V)^2$$

$$k_n = 0.0161 \frac{A}{V^2}$$

If you pick a point in the ohmic region, you'll get the same result - just use the ohmic-region equation

$$I_{ds} = k_n \left(V_{gs} - V_{th} - \frac{V_{ds}}{2} \right) V_{ds}$$



2) Draw the load-line for the circuit below. From the load line, determine the Q-point (V_{ds} , I_{ds}) when

$V_g = 0V$

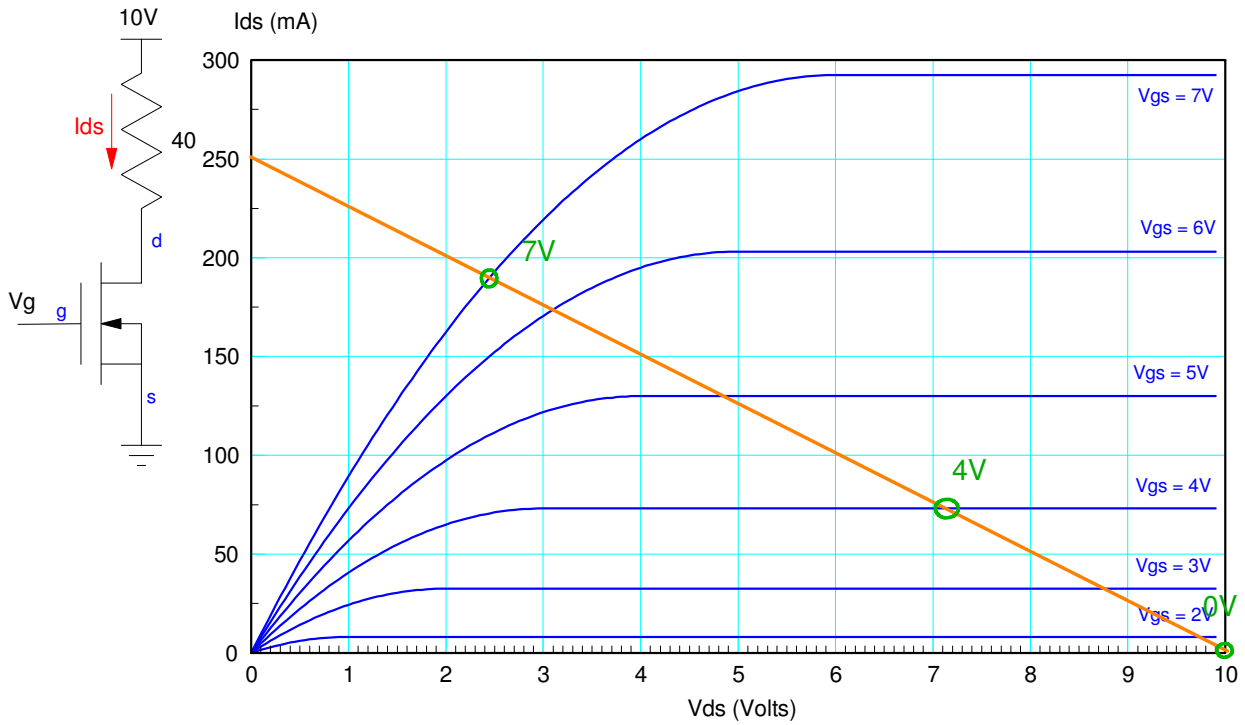
- $V_{ds} = 10V$
- $I_{ds} = 0mA$
- Off region

$V_g = 4V$

- $V_{ds} = 7.2V$
- $I_{ds} = 60mA$
- Saturated region

$V_g = 7V$

- $V_{ds} = 2.4V$
- $I_{ds} = 190mA$
- Ohmic region



MOSFET Switch

The characteristics for a IRF3205 MOSFET are

- Max Current = 110A continuous
- $R_{ds} = 0.008 \text{ Ohms @ } I_{ds} = 62A \text{ @ } V_{gs} = 10V$
- $V_{th} = 4.00V \text{ (max)}$

3) Determine the transconductance gain, k_n

In the ohmic region

$$I_{ds} = k_n \left(V_{gs} - V_{th} - \frac{V_{ds}}{2} \right) V_{ds}$$

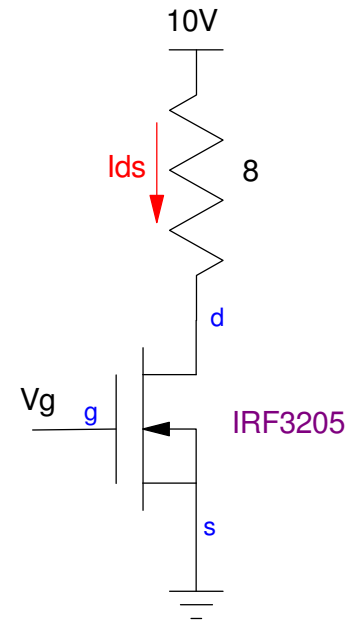
From the data

$$V_{ds} = 0.008 \Omega \cdot 62A = 0.496V$$

Plugging in numbers

$$62A = k_n \left(10V - 4V - \frac{0.496V}{2} \right) \cdot 0.496V$$

$$k_n = 21.732 \frac{A}{V^2}$$



4) Determine the voltages for the following circuit for

$$\mathbf{V_{in} = V_g = 0V}$$

Off region

$$V_{ds} = 10V, I_{ds} = 0$$

$$\mathbf{V_{in} = V_g = 5V}$$

Assume saturated region

$$I_{ds} = \left(\frac{k_n}{2}\right)(V_{gs} - V_{th})^2$$

$$I_{ds} = \left(\frac{21.732}{2}\right)(5V - 4V)^2$$

$$I_{ds} = 10.86A$$

$$V_{ds} = 10 - 8I_{ds} = -76.9V$$

That can't be, so assume ohmic region

$$I_{ds} = 21.732\left(5V - 4V - \frac{V_{ds}}{2}\right)V_{ds}$$

$$10 = 8I_{ds} + V_{ds}$$

Solving two equations for two unknowns

$$V_{ds} = 0.0589V, I_{ds} = 1.2427 \text{ Amps}$$

$$R_{ds} = V_{ds} / I_{ds} = 0.0474 \text{ Ohms}$$

$$\mathbf{V_{in} = V_g = 10V}$$

Assume Ohmic region

$$I_{ds} = 21.732\left(10V - 4V - \frac{V_{ds}}{2}\right)V_{ds}$$

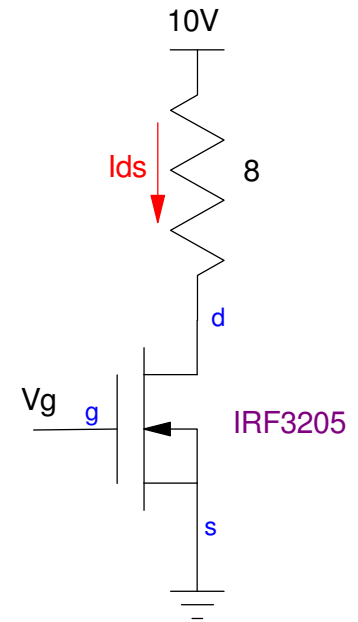
$$10 = 8I_{ds} + V_{ds}$$

Solving

$$V_{ds} = 0.0096V$$

$$I_{ds} = 1.2488A$$

$$R_{ds} = V_{ds} / I_{ds} = 0.0077 \text{ Ohms}$$



5) Simulate this circuit in CircuitLab using an IRF3205 MOSFET. (you may need to adjust the parameters to match your calculations for k_n and V_{th}). Determine the voltages and currents when

$$V_{in} = V_g = 0V$$



CMOS Logic

6) Design a CMOS gate to implement the function: $Y(A, B, C, D)$

Circle the zeros (ones also work)

Y(A,B,C,D)		CD			
		00	01	11	10
AB	00	1	1	1	x
	01	0	0	0	1
	11	1	x	1	0
	10	x	1	x	0

$$\bar{Y} = \bar{A}\bar{B}\bar{C} + \bar{A}BD + A\bar{C}\bar{D}$$

From DeMorgan's law

$$Y = (A + \bar{B} + C)(A + \bar{B} + \bar{D})(\bar{A} + \bar{C} + D)$$

