

ECE 320 - Homework #9

MOSFET Switches, CMOS logic. Due Monday, March 21st

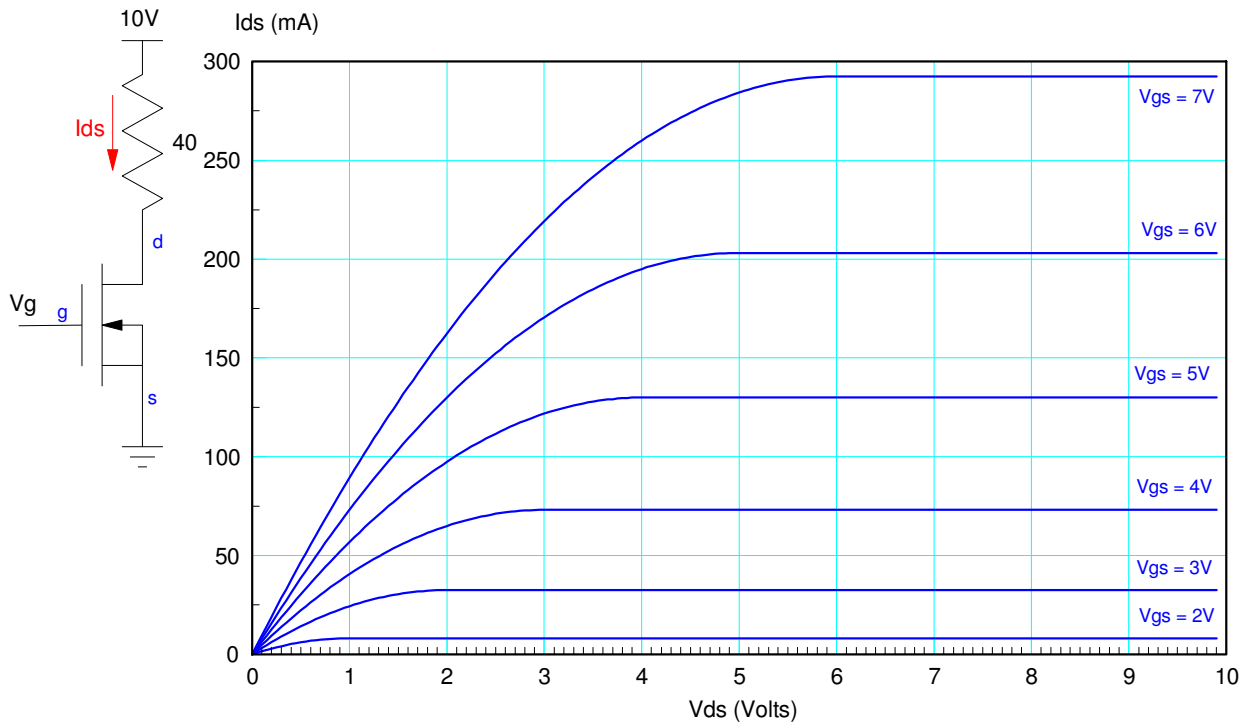
MOSFETs

1) The VI characteristics for an n-channel MOSFET are shown below.

- Label the off / ohmic / and saturated regions
- Determine the transconductance gain, k_n . Assume $V_{th} = 1.00V$

2) Draw the load-line for the circuit below. From the load line, determine the Q-point (V_{ds} , I_{ds}) when

- $V_g = 0V$
- $V_g = 4V$
- $V_g = 7V$



MOSFET Switch

The characteristics for a IRF3205 MOSFET are

- Max Current = 110A continuous
- $R_{ds} = 0.008 \text{ Ohms @ } 62\text{A @ } 10\text{V}$
- $V_{th} = 4.00\text{V (max)}$

3) Determine the transconductance gain, k_n

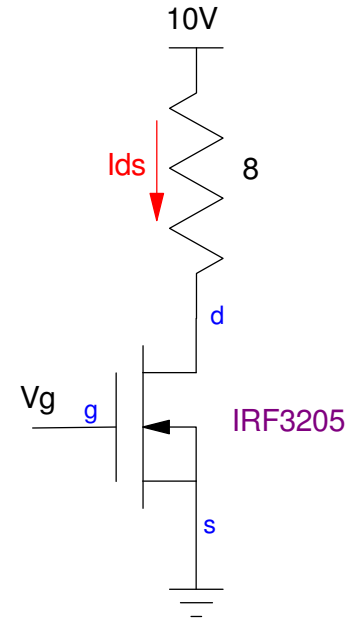
4) Determine the voltages for the following circuit for

- $V_{in} = V_g = 0\text{V}$
- $V_{in} = V_g = 5\text{V}$
- $V_{in} = V_g = 10\text{V}$

5) Simulate this circuit in CircuitLab using an IRF3205 MOSFET. (you may need to adjust the parameters to match your calculations for k_n and V_{th}).

Determine the voltages and currents when

- $V_{in} = V_g = 0\text{V}$
- $V_{in} = V_g = 5\text{V}$
- $V_{in} = V_g = 10\text{V}$
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CMOS Logic

6) Design a CMOS gate to implement the function: $Y(A, B, C, D)$

Y(A,B,C,D)		CD			
		00	01	11	10
AB	00	1	1	1	x
	01	0	0	0	1
	11	1	x	1	0
	10	x	1	x	0