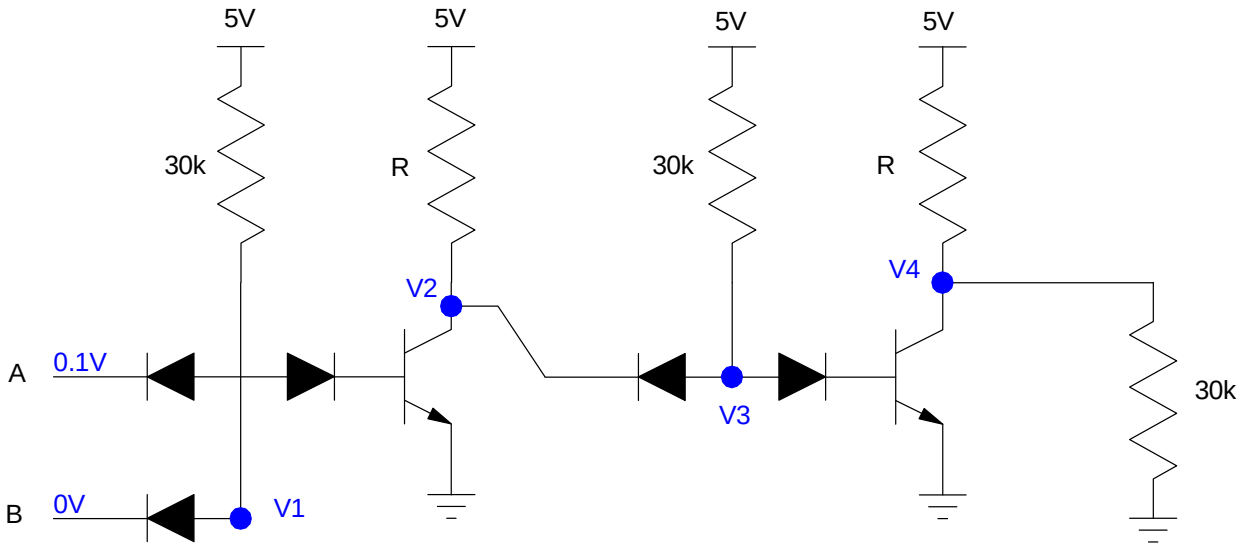
DTL Logic Gate:

4) Determine the voltges and currents for the following DTL gate. Assume

- Ideal 3904 transistors (Vbe = 0.7V, Vce(sat) = 0.2V, Bmin = 100)
- Ideal silicon diodes (Vf = 0.7V)
- R = 1000 + 100(Birth Month) + (Birth Day). For example, May 14th gives R = 1514 Ohms.

R	V1	V2	V3	V4



Open Collector Logic

5) Determine the voltages and currents for the following circuit. Assume

- Ideal silicon diodes (Vf = 0.7V)
- Vbe = 0.7V
- b = 100
- R = 1000 + 100(Birth Month) + (Birth Day). For example, May 14th gives R = 1514 Ohms.

R	I1	I2	I3	V4