

# ECE 320 - Quiz #8 - Name \_\_\_\_\_

DTL, TTL Logic, MOSFETs.

## DTL Logic Gate:

Determine the voltages and currents for the following DTL gate. Assume

- Ideal 3904 transistors ( $V_{be} = 0.7V$ ,  $V_{ce(sat)} = 0.2V$ , gain = 100)
- Ideal silicon diodes ( $V_f = 0.7V$ )
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# MOSFETs

For the following MOSFET circuit, assume

- $k_n = 0.5 \text{ A/V}^2$
- $V_{th} = 2.00\text{V}$

Determine the operation point ( $V_{ds}$ ,  $I_{ds}$ ) for  $V_g = 10\text{V}$

R 1000 + 100*mo + day	$V_{ds}$ $V_g = 10\text{V}$	$I_{ds}$ $V_g = 10\text{V}$

Ohmic Region:  $V_{ds} < V_{gs} - V_{th}$

$$I_{ds} = k_n \left( V_{gs} - V_{th} - \frac{V_{ds}}{2} \right) V_{ds}$$

Saturated Region:  $V_{ds} > V_{gs} - V_{th}$

$$I_{ds} = \frac{k_n}{2} (V_{gs} - V_{th})^2$$

